IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

os manufacture licant(s): Lancaster nal No.: 09/689,442

Filed: October 12, 2000

Title: Semiconductor Non-Volatile Memory **Device Having Improved Write Speed** Examiner: Meier, S.

Group Art Unit: 2822

RESPONSE TO OFFICE ACTION

Assistant Commissioner for Patents Washington, D.C. 20231

Dear Sir:

The following is submitted in response to the Office Action dated September 12, 2001, and is currently due November 13, 2001, with a two-month extension.

REMARKS

Entry of the following amendments is respectfully requested.

2800 HAIL ROOM

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10	1 hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelopaddressed to: Assistant Commissioner for Patents, Washington, D.C. 20231.	— ре
	Date of Deposit: NOVEMBEX 13, 2001	

Typed/Printed Name: BRANLEY T. SAKO

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